Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	416	714/727.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/16 11:30
L2	67	714/730.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/16 11:30
L3	1454	327/156.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/16 11:33
L4	841	714/733.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/16 11:34
L5	3243	714/724.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/16 11:35
L6	653	714/42.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/16 11:35
L7	1706	714/25.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/16 11:36
S1	1248	714/726.CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/13 17:02
S4	24	(normal with test$3 with mode) and (IC or (integrated adj circuit)) and (scan adj test$3) and (reset$4 with(flip-flop$1 or register$1) with normal with test$3 with mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/14 13:12

S5	24	(normal with test$3 with mode) and ((IC or (integrated adj circuit)) with test$3) and (scan adj test$3) and (reset$4 with(flip-flop$1 or register$1) with normal with test$3 with mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/14 17:12
S6	3	(normal with test$3 with mode) and ((IC or (integrated adj circuit)) with (scan adj test$3)) and (reset$4 with(flip-flop$1 or register$1) with normal with test$3 with mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/14 13:13
S9	18	(normal with test$3 with mode) and ((IC or (integrated adj circuit)) with (scan adj test$3)) and (reset$4 with(flip-flop$1 or register$1) with mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 11:33
S12	5	(control$4 with access with memory with (test adj mode)) and (memory with (register$1 or flip-flop$1)) and (scan adj test$3) and (integrated adj circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 14:05
S13	1	(control$4 with access with memory with (test adj mode) with prohibit$3) and (memory with (register$1 or flip-flop$1)) and (scan adj test$3) and (integrated adj circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 13:36
S14	7	(control$4 with access with memory with (test adj mode)) and (memory with (register$1 or flip-flop$1)) and (scan$4) and (integrated adj circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 13:51
S15	1	(control$4 with access with memory with (test adj mode) with prohibit$4) and (integrated adj circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 13:48
S16	2	(access with memory with (test adj mode) with prohibit$4) and (integrated adj circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 13:48
S17	18	(control$4 with access with memory with (test adj mode)) and (memory with flip-flop$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 13:52
S18	8	(control$4 with access with memory with (test adj mode)) and (memory with flip-flops)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 13:53

S19	1	(control$4 with access with memory with (test adj mode)) and (memory with flip-flops) and scan$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 13:53
S23	1	(memory with connect$3 with flip-flops) and (memory with (test adj mode) with access with control$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 14:14
S24	4	(memory with connect$3 with flip-flops) and (memory with (test adj mode) with access)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 14:16
S25	11	(memory with connect$3 with registers) and (memory with (test adj mode) with access)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 14:24
S29	2	((transition adj detect$3) with timing with level with mode with signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 14:57
S30	1	((transition adj detect$3) with timing with (logic$2 adj level) with mode with signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 14:58
S31	1	((transition adj detect$3) with timing with (logic$2 adj level) with mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 14:58
S32	2	((transition adj detect$3) with timing with level with mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 14:59
S33	1	((transition adj timing) with detect$3 with level with mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 15:00
S34	5	((transition adj timing) with detect$3 with mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 17:19

S36	4	(scan adj mode) with (detect$3 adj signal) with edge	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 17:22
S40	12	(scan adj mode adj detect$3) with signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 17:24
S42	4	(scan adj mode) with detect$3 with signal with edge	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 17:25
S43	4	(scan adj mode) with detect$3 with signal with transition	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 17:35
S44	1	(detect$3 adj transition adj timing) with (scan adj mode) with signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 17:35
S45	1	(detect$3 adj transition adj timing) with (mode) with signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 17:36
S46	18	(detect$3 adj transition adj timing) with signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/15 17:36